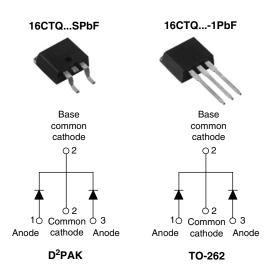




Vishay High Power Products

Schottky Rectifier, 2 x 8 A



PRODUCT SUMMARY				
I _{F(AV)} 2 x 8 A				
V_{R}	60 to 100 V			

FEATURES

- 175 °C T_J operation
- Center tap configuration





RoHS*

- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free ("PbF" suffix)
- Designed and qualified for Q101 level

DESCRIPTION

This center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	16	A		
V _{RRM}		60 to 100	V		
I _{FSM}	$t_p = 5 \mu s sine$	850	A		
V _F	8 Apk, T _J = 125 °C (per leg)	0.58	V		
T _J	Range	- 55 to 175	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	16CTQ060SPbF 16CTQ060-1PbF	16CTQ080SPbF 16CTQ080-1PbF	16CTQ100SPbF 16CTQ100-1PbF	UNITS
Maximum DC reverse voltage	V_{R}	60	80	100	V
Maximum working peak reverse voltage	V_{RWM}	00	60	100	V

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average per leg		50 % duty cycle at T _C = 148 °C, rectangular waveform		8	۸
See fig. 5 per device	I _{F(AV)}			16	А
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	850	^
non-repetitive surge current per leg See fig. 7	, , ,		V _{RRM} applied	275	A
Non-repetitive avalanche energy per leg E _A		T _J = 25 °C, I _{AS} = 0.50 A, L = 60 mH		7.50	mJ
Repetitive avalanche current per leg I _{AR}		Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		0.50	Α

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply

16CTQ...SPbF/16CTQ...-1PbF Series

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V _{FM} ⁽¹⁾	8 A	T _J = 25 °C	0.72	V
Maximum forward voltage drop per leg		16 A		0.88	
See fig. 1		8 A	T _J = 125 °C	0.58	
		16 A		0.69	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	0.55	mA
See fig. 2	'RM '''	T _J = 125 °C		7.0	
Threshold voltage	V _{F(TO)}	T _J = T _J maximum		0.415	V
Forward slope resistance	r _t			11.07	mΩ
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		500	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body 8.0		nΗ	
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V		V/µs	

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER SY		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 175	°C
Maximum thermal resistance, junction to case per leg		D	HthJC DC operation HthCs Mounting surface, smooth and greased		
Maximum thermal resistance, junction to case per package		H _{th} JC			°C/W
Typical thermal resistance, case to heatsink		R _{thCS}			
Approximate weight				2	g
				0.07	OZ.
Mounting torque	minimum			6 (5)	kgf · cm
Mounting torque	maximum			12 (10)	(lbf ⋅ in)
Madding davis		Case style D ² PAK	16CT	QS	
Marking device			Case style TO-262	16CT	Q1



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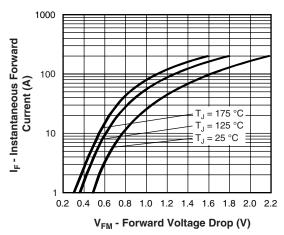


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

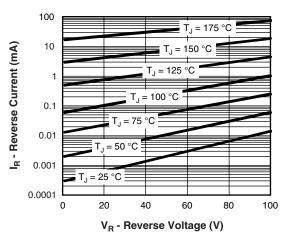


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

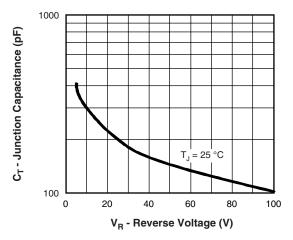


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

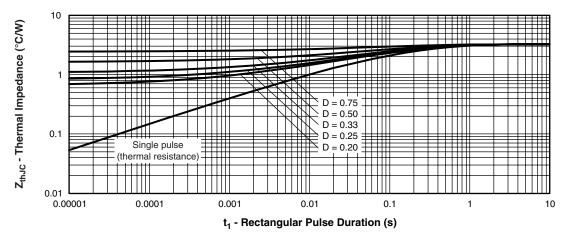


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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Vishay High Power Products Schottky Rectifier, 2 x 8 A



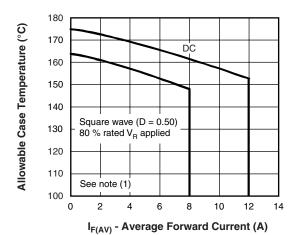


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

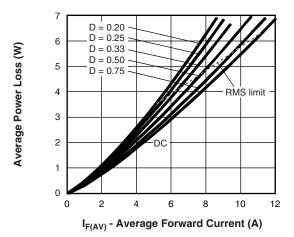


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

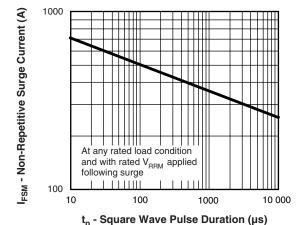


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

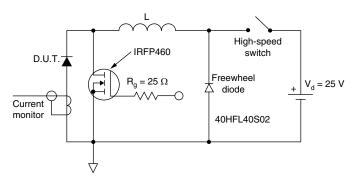


Fig. 8 - Unclamped Inductive Test Circuit

Note

 $\begin{array}{l} \text{(1)} \ \ \text{Formula used:} \ T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \ \text{at} \ (I_{F(AV)}/D) \ \text{(see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \ \text{(1 - D)}; \ I_R \ \text{at} \ V_{R1} = 80 \ \% \ \text{rated} \ V_R \ \text{applied} \\ \end{array}$

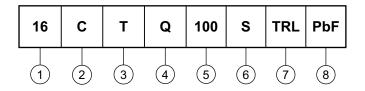


16CTQ...SPbF/16CTQ...-1PbF Series

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ORDERING INFORMATION TABLE

Device code



Current rating (16 A)

2 - Circuit configuration

C = Common cathode

3 - T = TO-220

- Schottky "Q" series 060 = 60 V

5 - Voltage ratings - 080 = 80 V 100 = 100 V

6 - • S = D²PAK

• -1 = TO-262

7 - • None = Tube (50 pieces)

• TRL = Tape and reel (left oriented - for D²PAK only)

• TRR = Tape and reel (right oriented - for D²PAK only)

8 - • None = Standard production

• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95014				
Part marking information	http://www.vishay.com/doc?95008			
Packaging information	http://www.vishay.com/doc?95032			
SPICE model	http://www.vishay.com/doc?95279			



Vishay

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